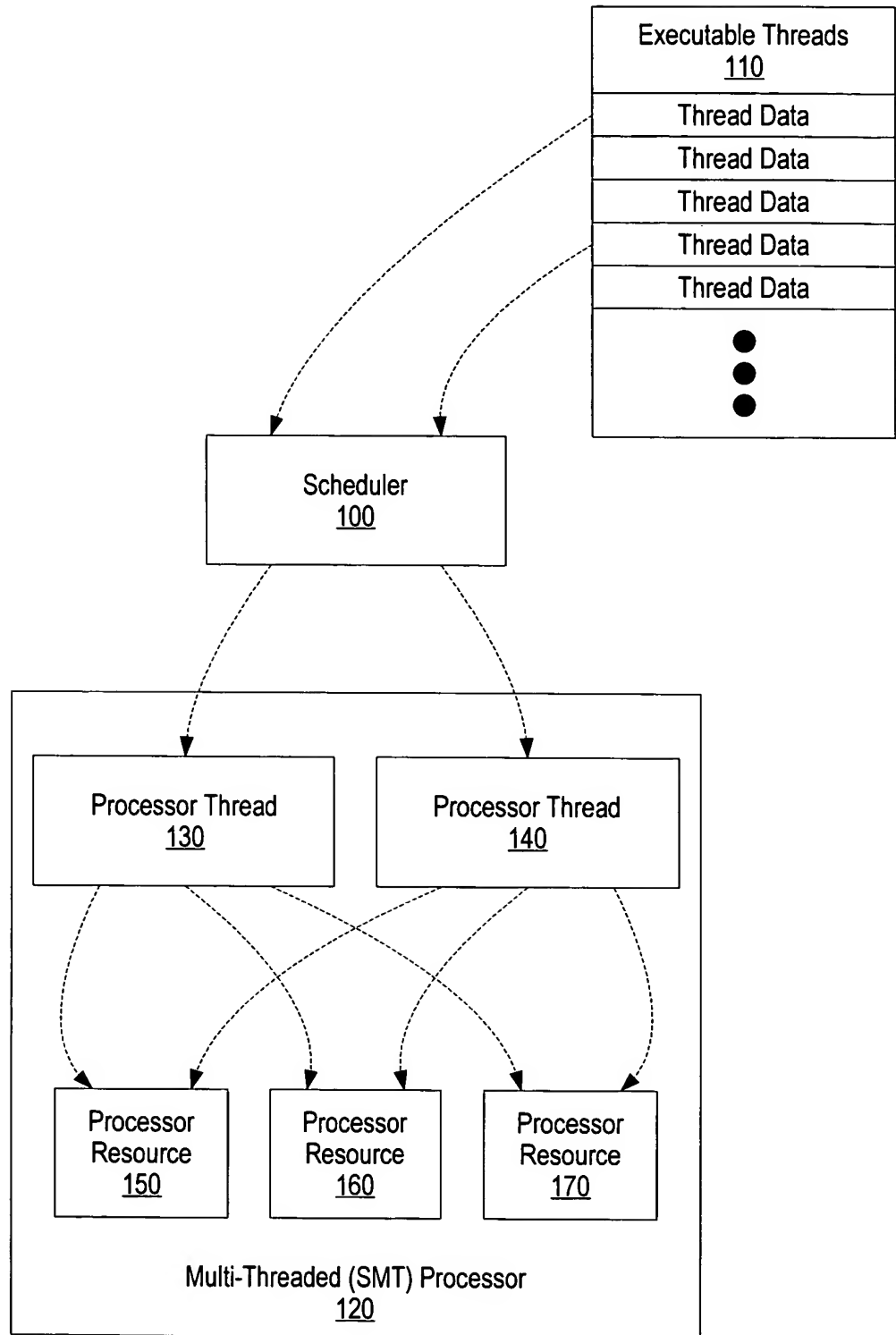
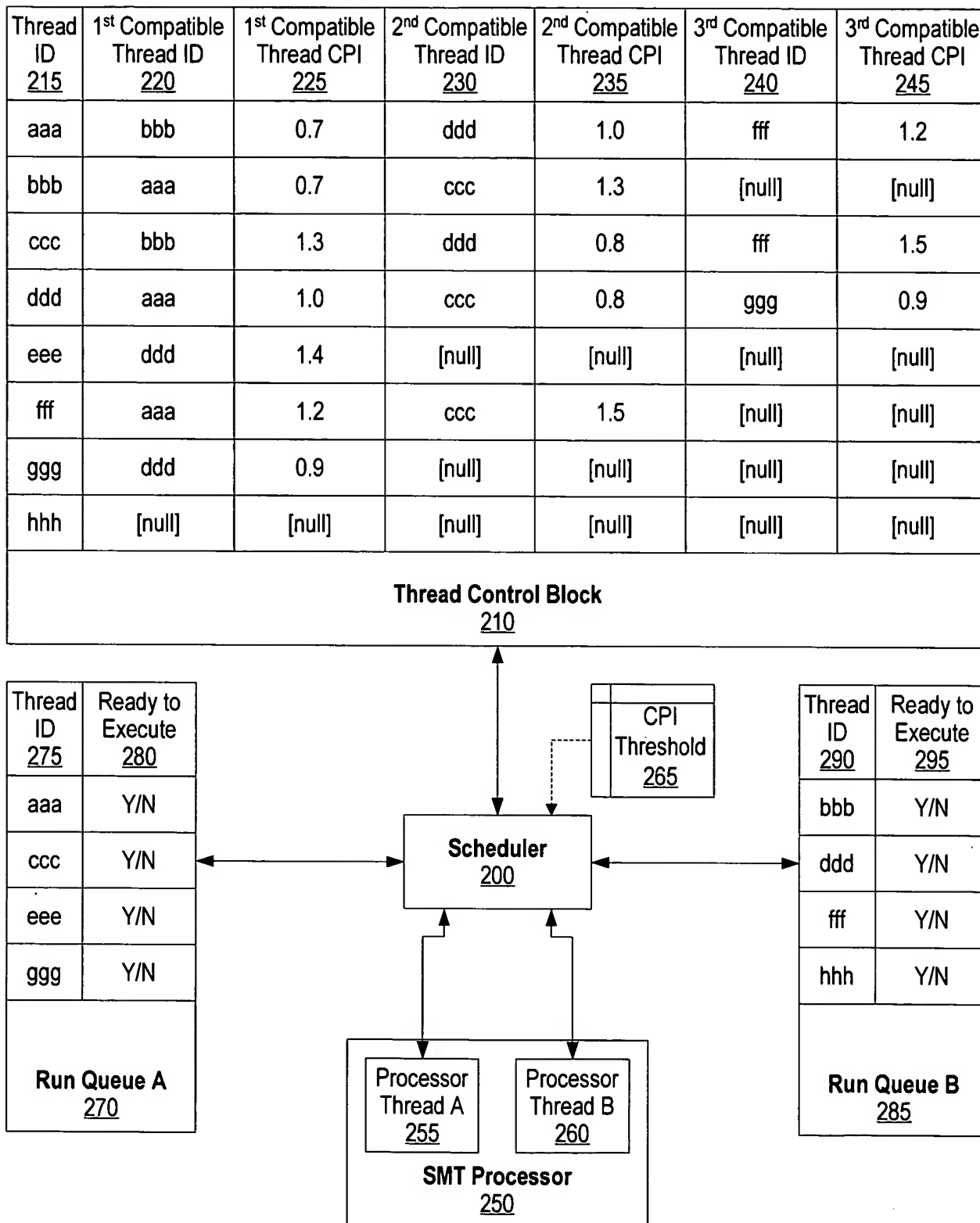


System and Method for CPI Load Balancing in SMT Environments

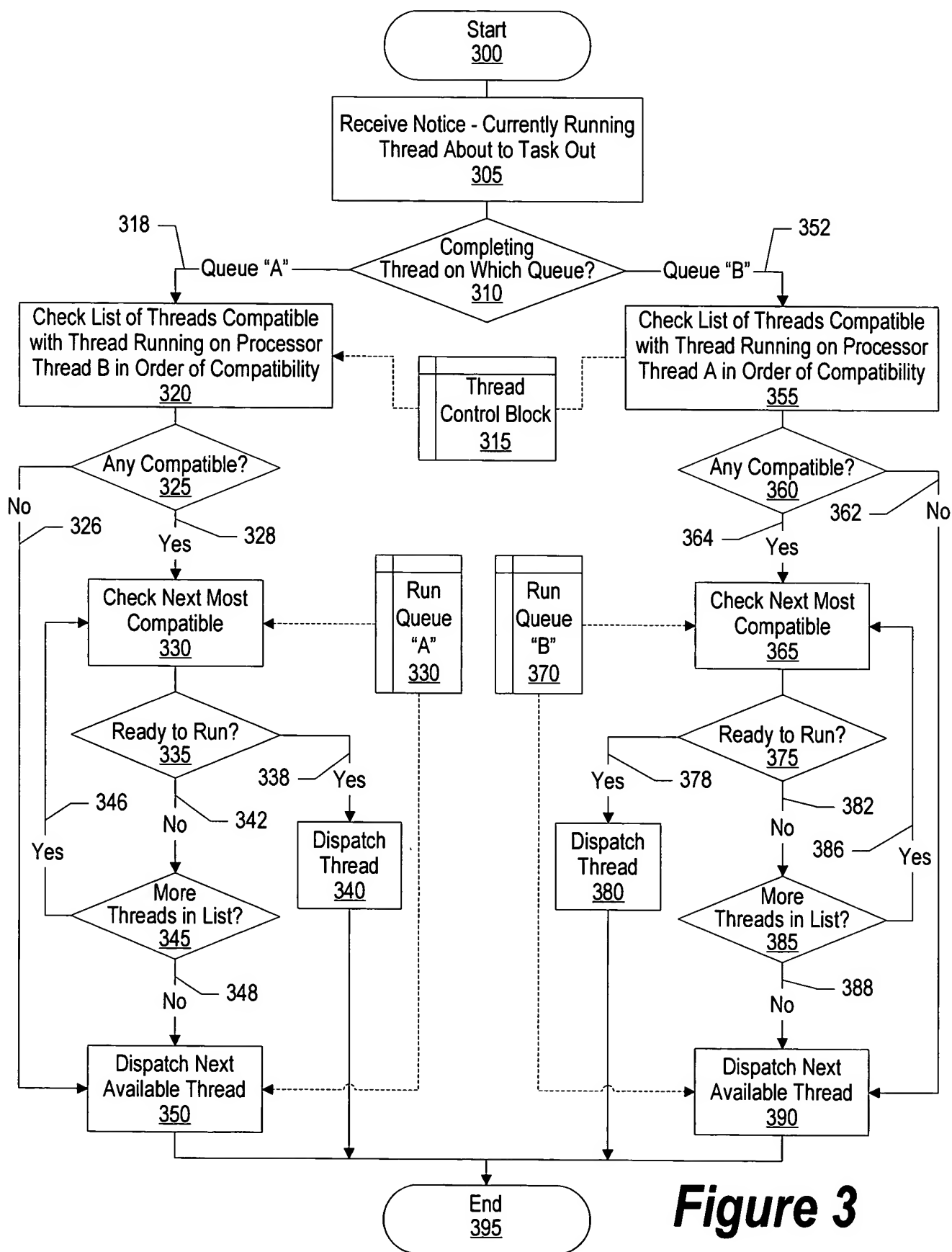
1/12

**Figure 1**

2 / 12

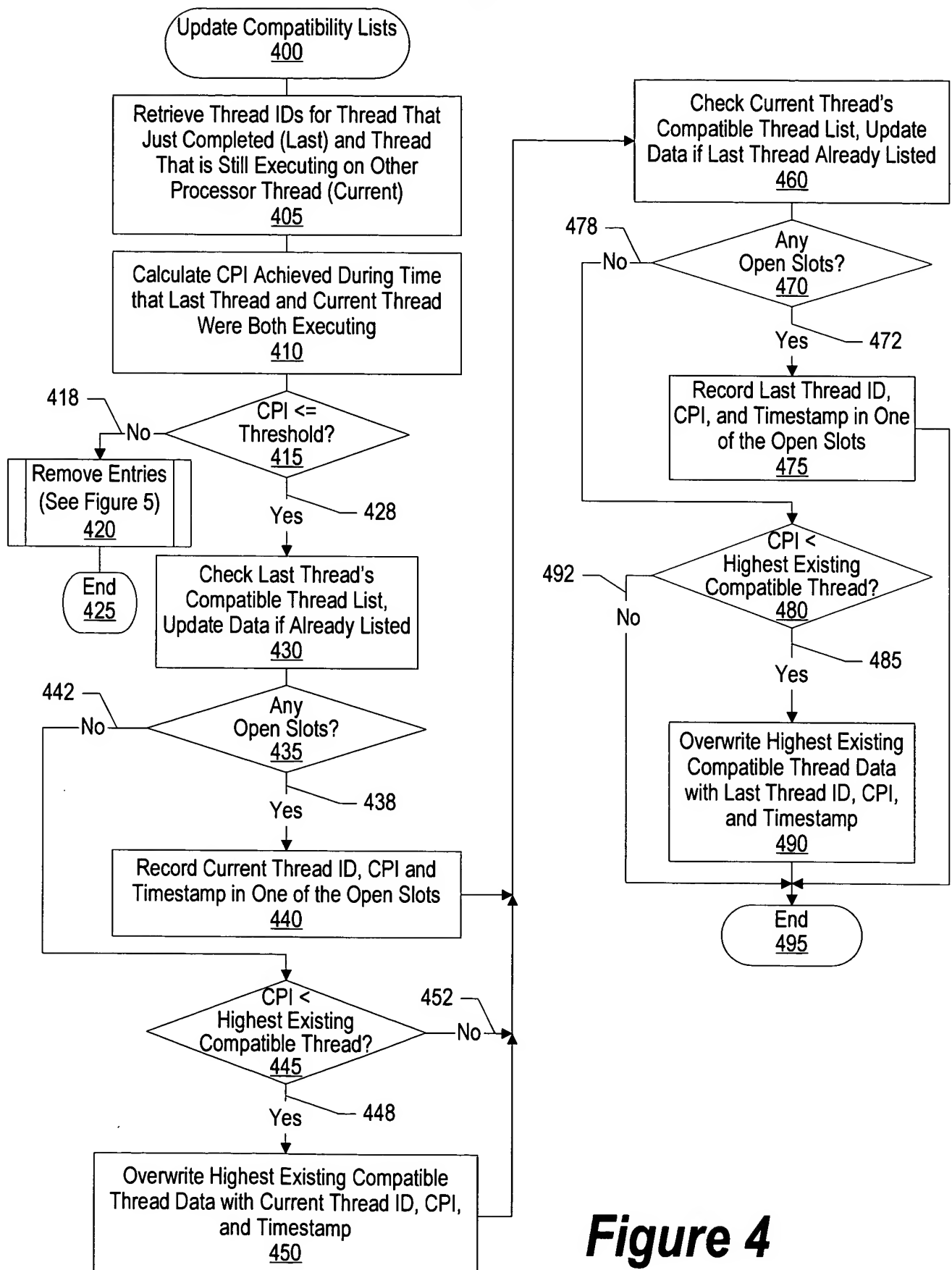
**Figure 2**

3 / 12

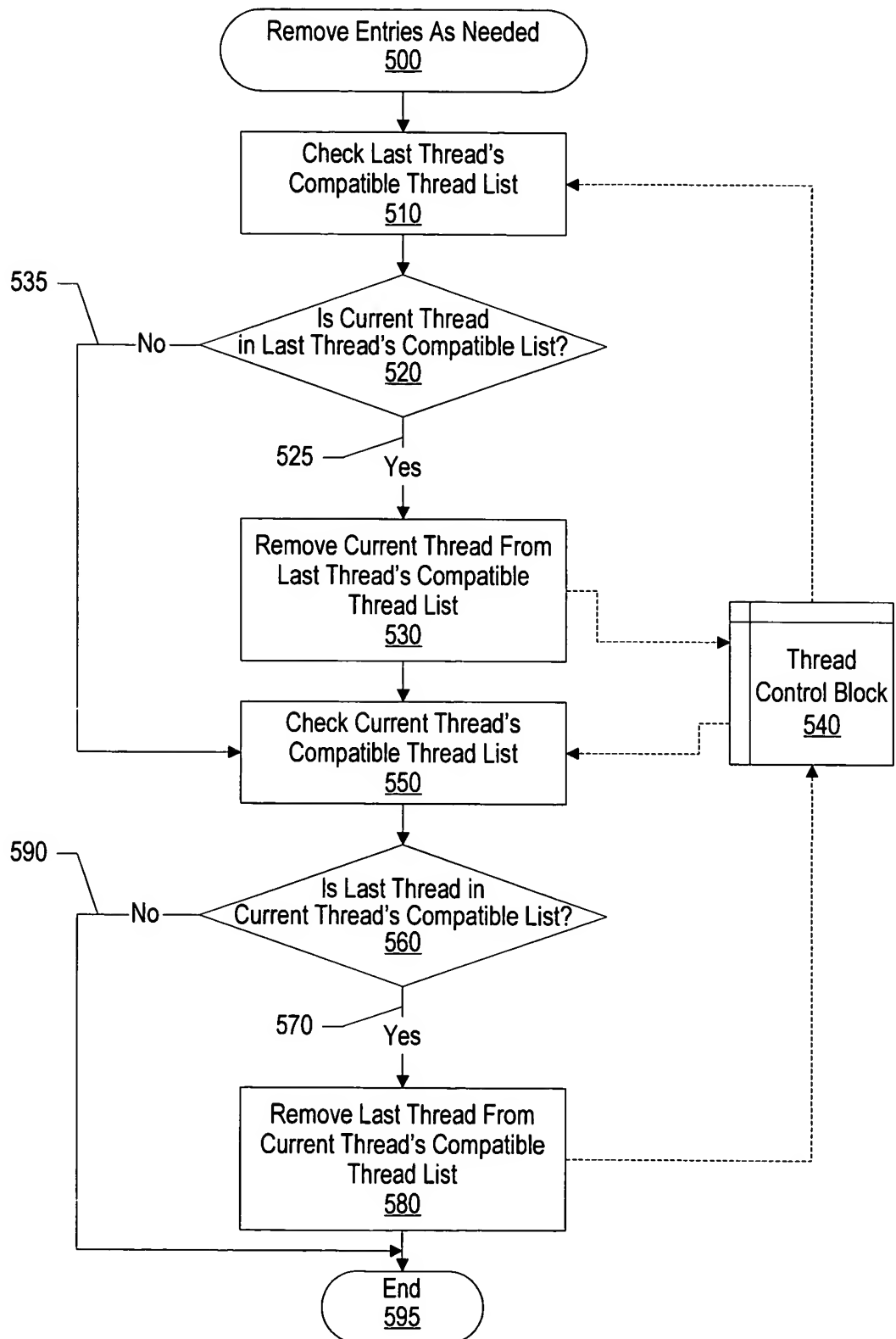
**Figure 3**

System and Method for CPI Load Balancing in SMT Environments

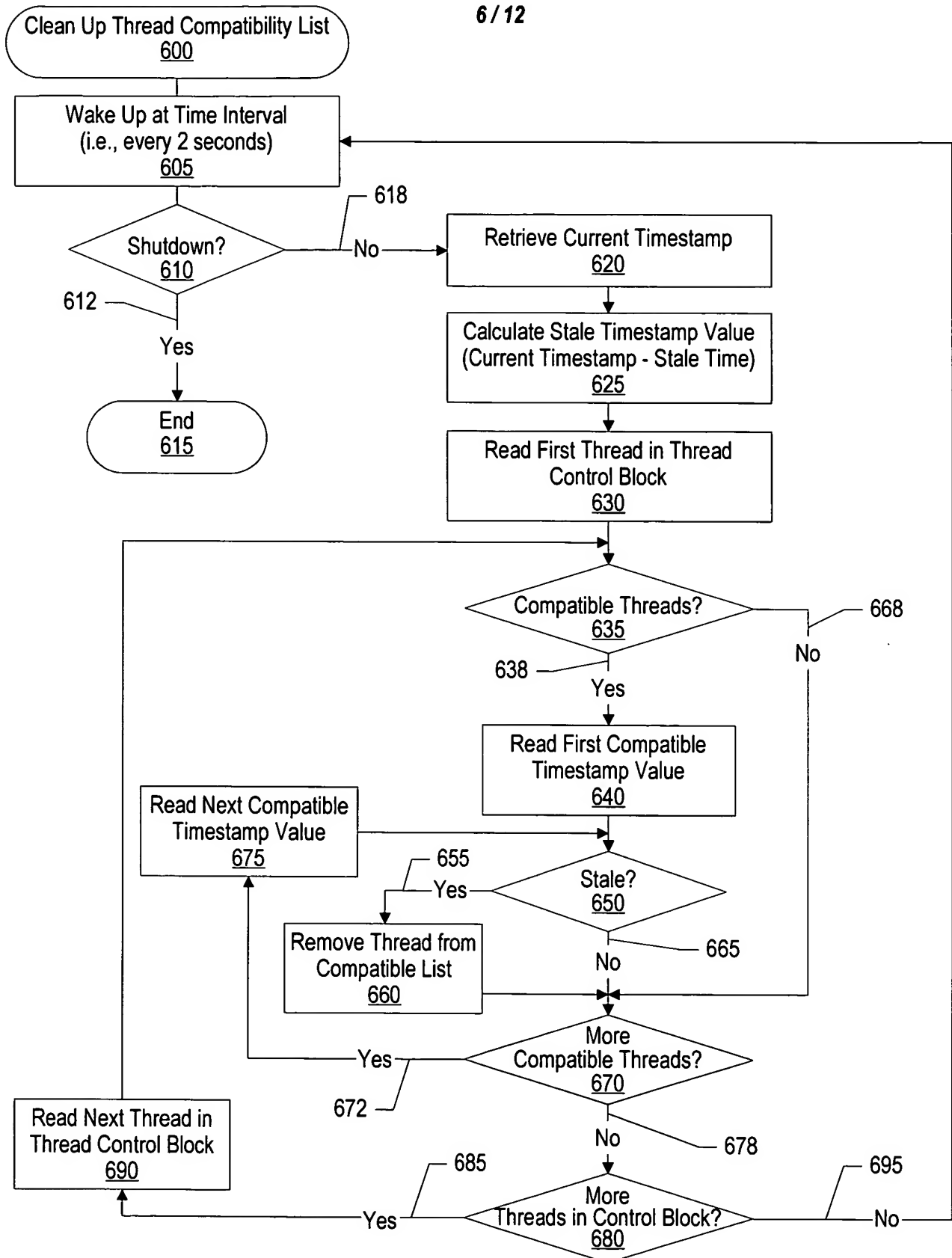
4 / 12

**Figure 4**

5/12

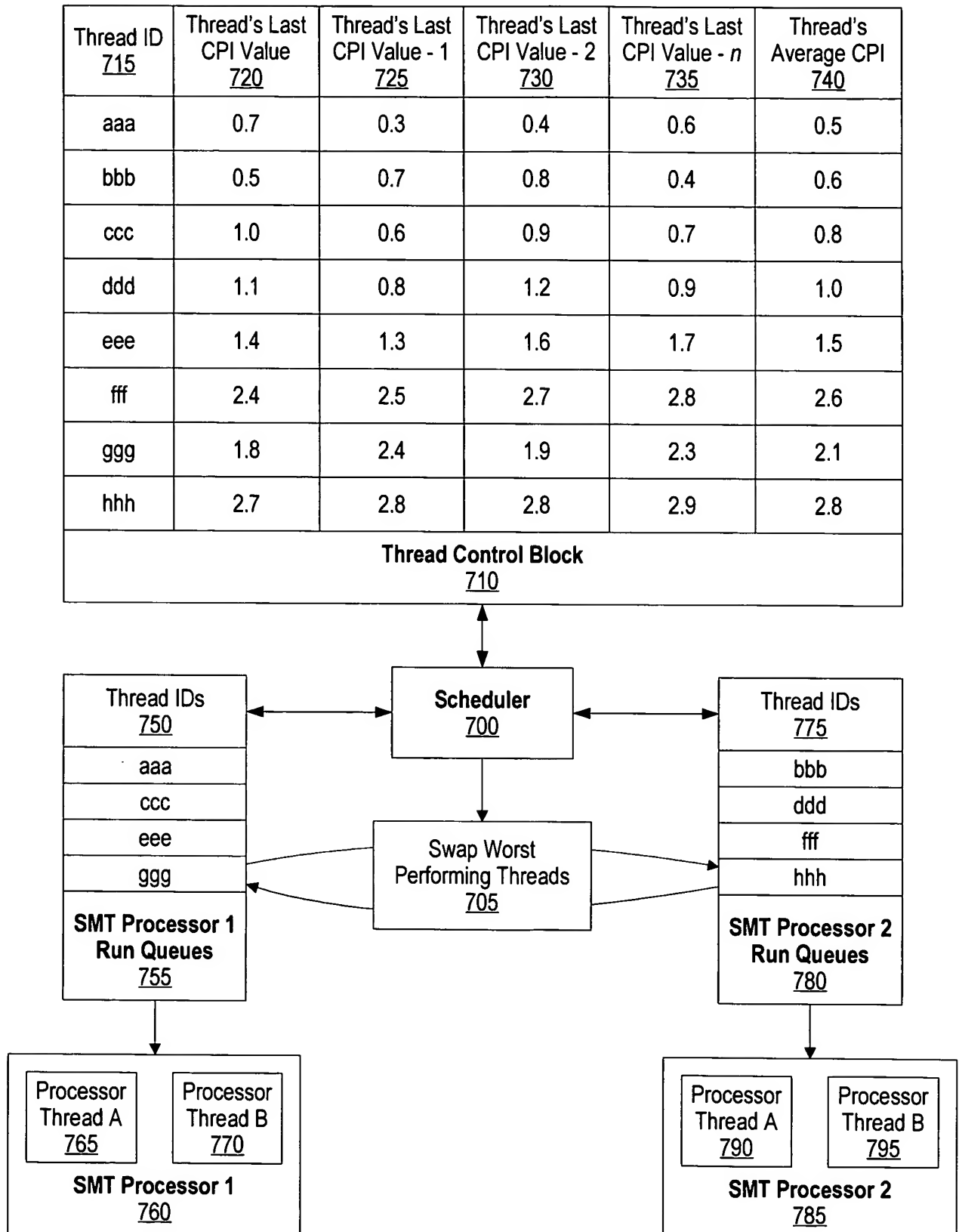
**Figure 5**

6 / 12

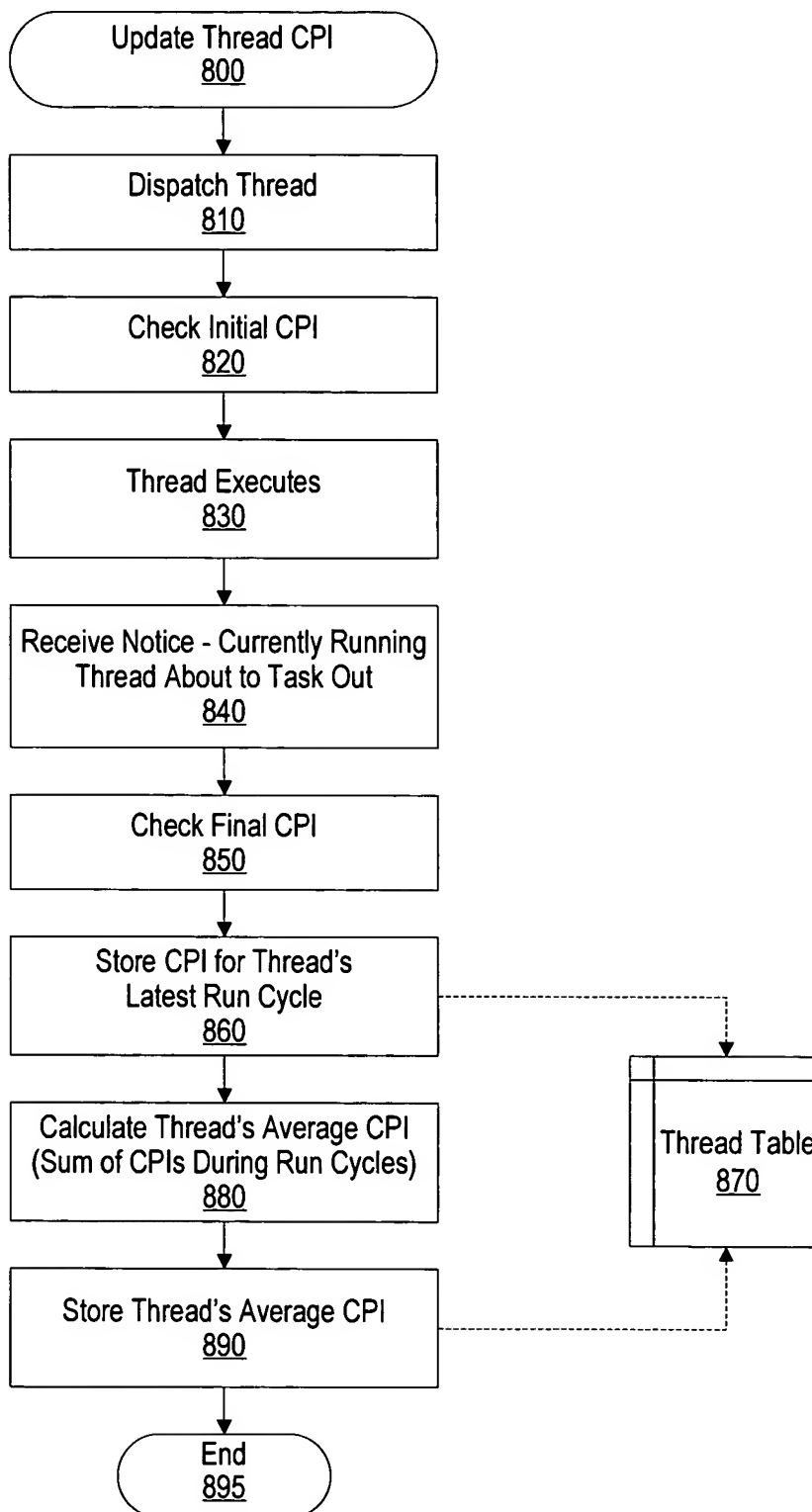
**Figure 6**

System and Method for CPI Load Balancing in SMT Environments

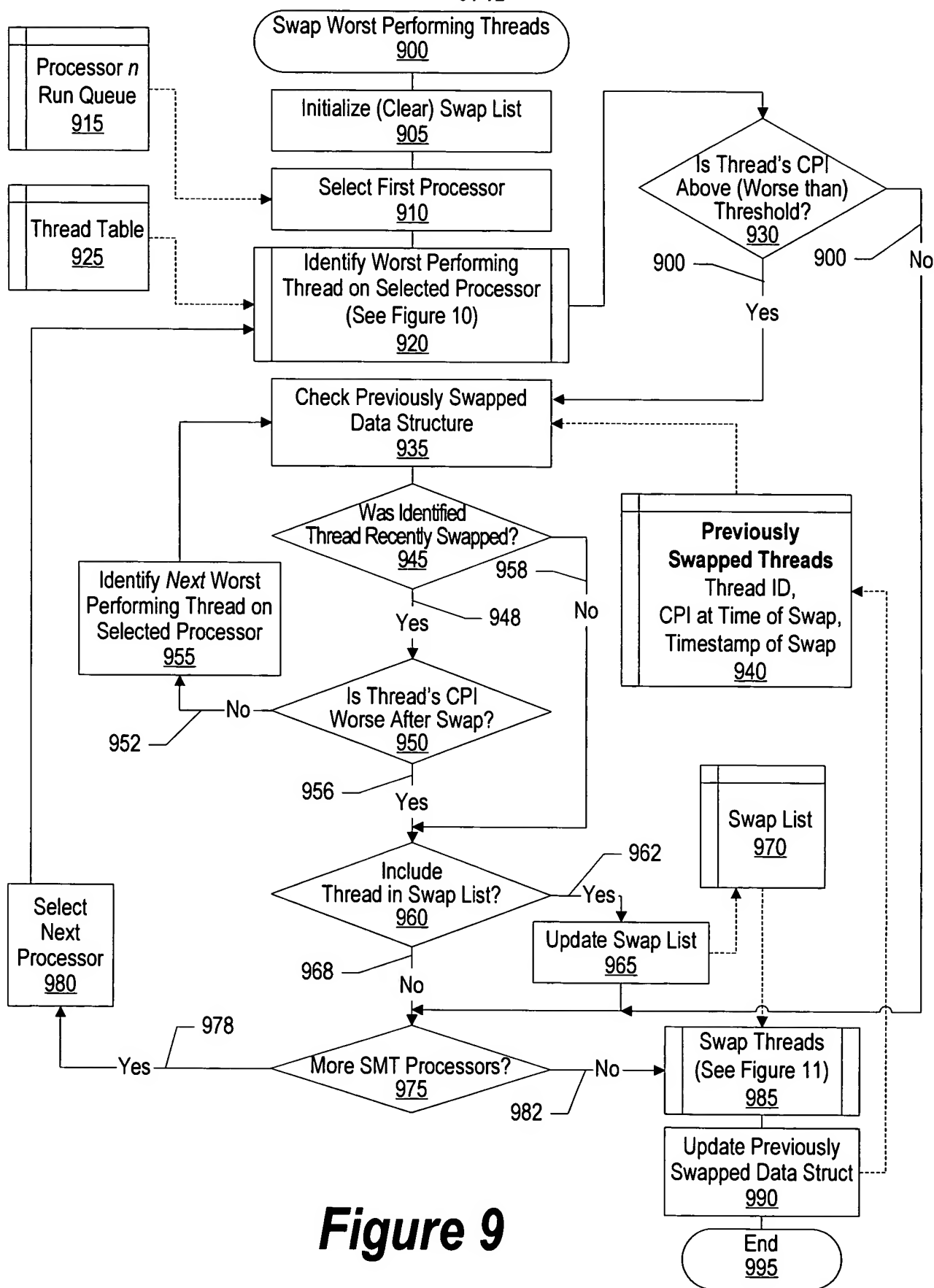
7/12

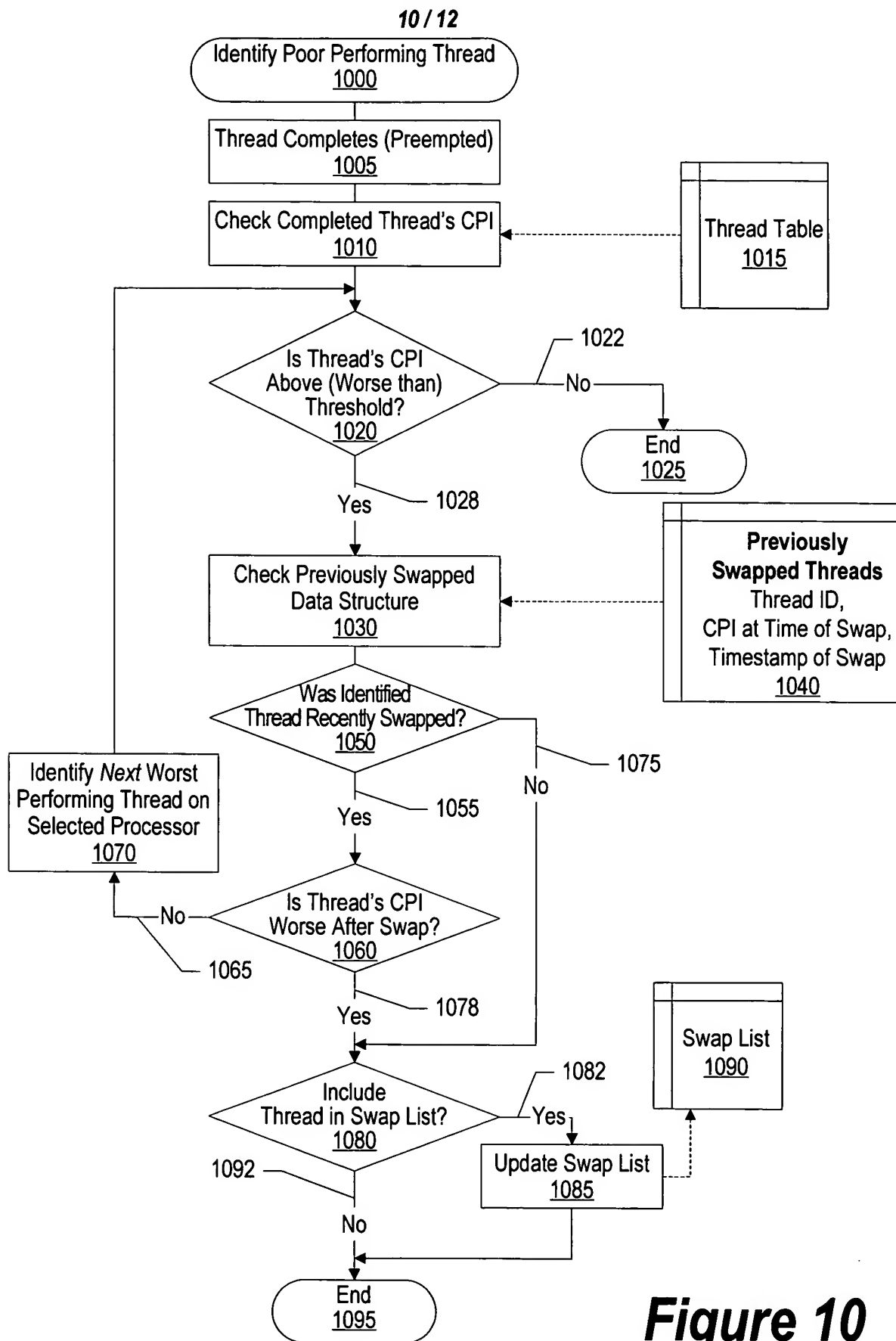
**Figure 7**

8 / 12

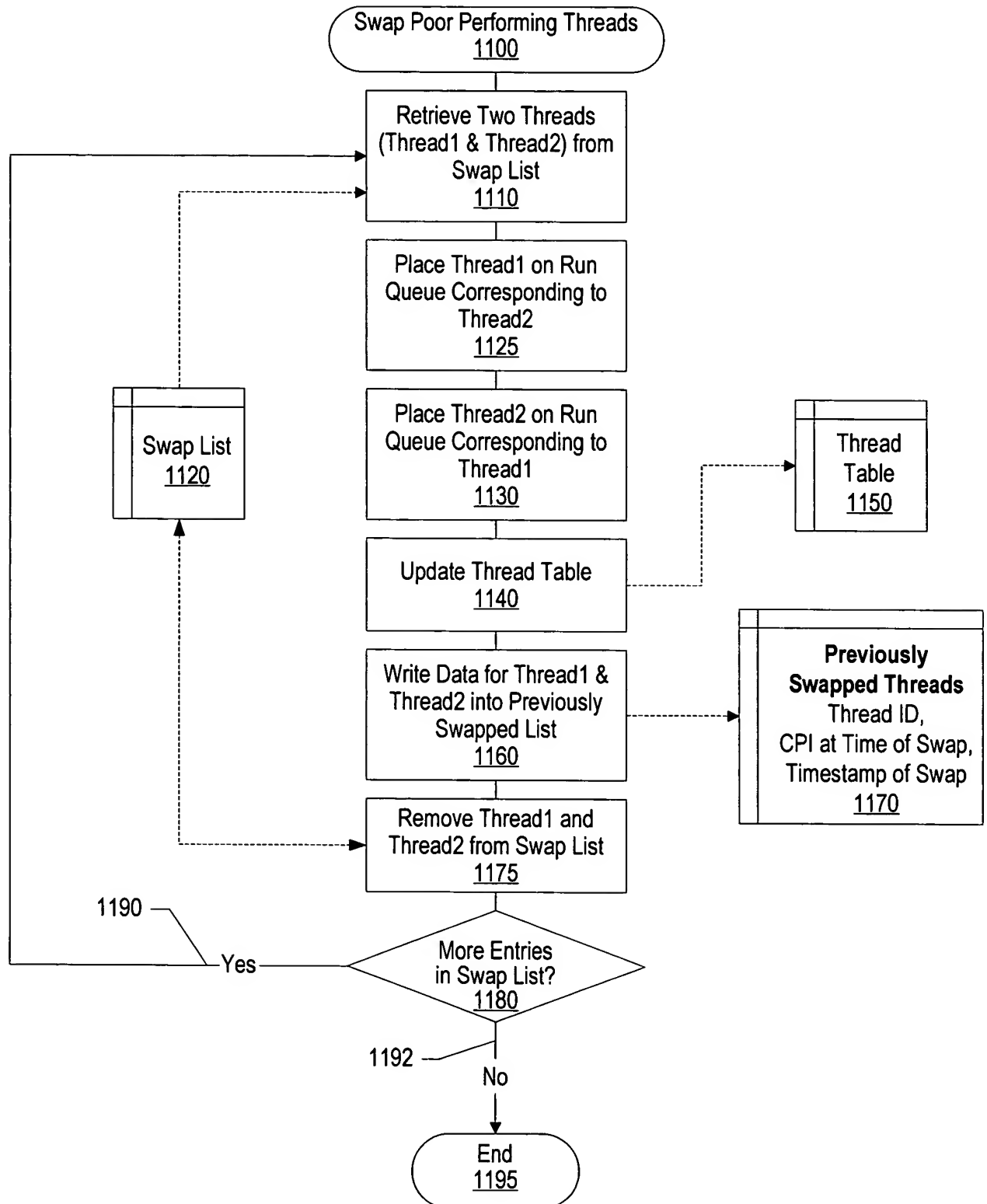
**Figure 8**

9/12

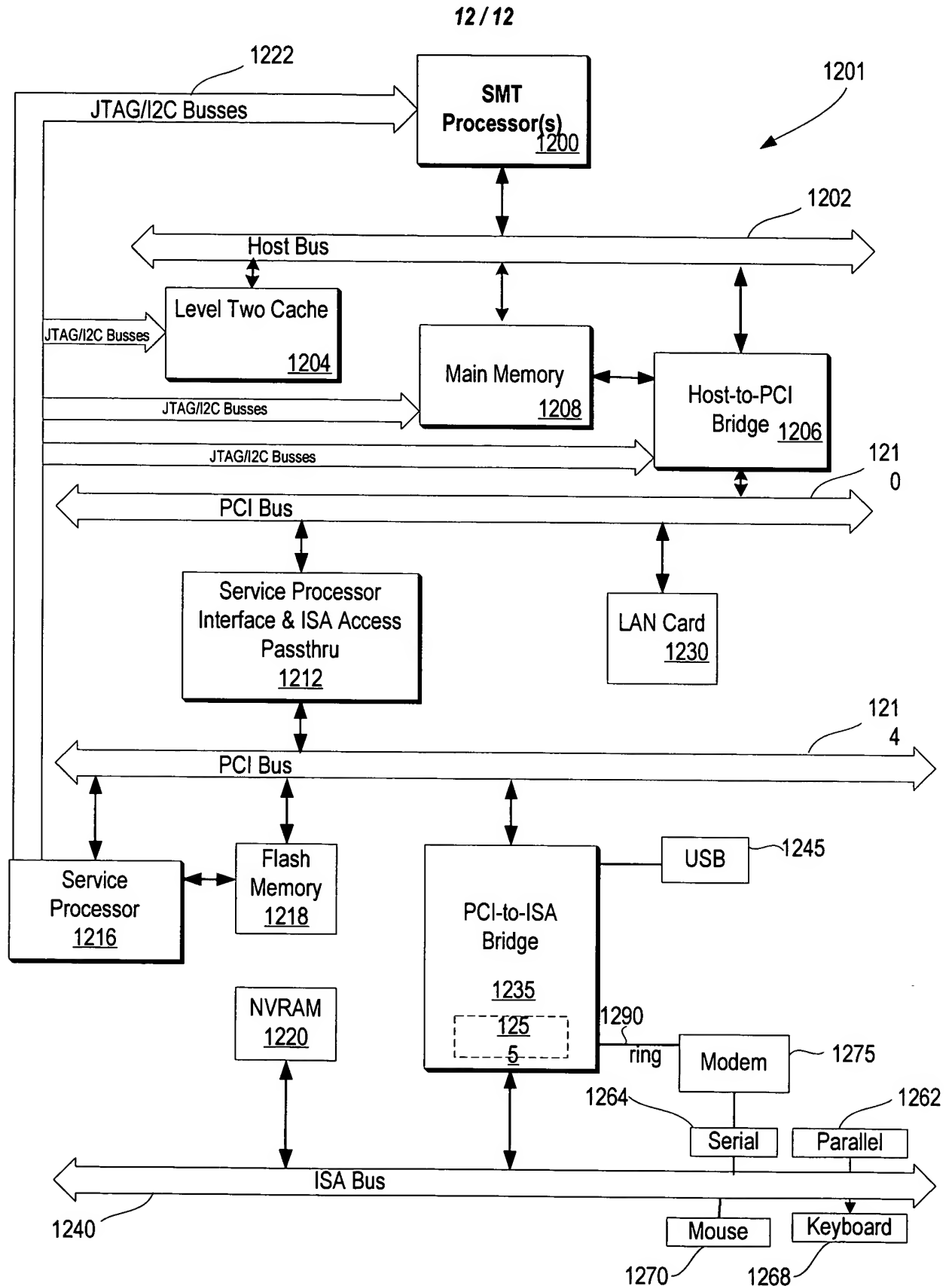
**Figure 9**



11 / 12

**Figure 11**

System and Method for CPI Load Balancing in SMT Environments

**Figure 12**